

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended) A plasma display panel, comprising:

- a transparent electrode;
- a metal bus electrode;
- a first light shielding layer formed between the transparent electrode and the metal bus electrode on each discharge cell; and
- a second light shielding layer formed between the adjacent discharge cells,

wherein the first light shielding layer and the second light shielding layer are different from each other in at least one of a thickness thereof and a concentration of a pigment thereof, and wherein the first light shielding layer and the second light shielding layer are connected to each other.

2. (Canceled)

3. (Original) The plasma display panel according to claim 1, further comprising:

- a substrate having the transparent electrode formed thereon,

wherein the second light shielding layer is commonly connected to the transparent electrodes formed in each of the adjacent discharge cells.

4. (Original) The plasma display panel of claim 1, further comprising:  
a substrate having the transparent electrode formed thereon,  
wherein the second light shielding layer is electrically connected to the transparent electrodes formed in each of the adjacent discharge cells.

5. (Previously Presented) The plasma display panel of claim 1, wherein the thickness of the first light shielding layer is thinner than the thickness of the second light shielding layer.

6. (Previously Presented) The plasma display panel of claim 5, wherein the thickness of the first light shielding layer is thinner by about  $0.1\mu\text{m} \sim 2\mu\text{m}$  than the thickness of the second light shielding layer.

7. (Previously Presented) The plasma display panel of claim 1, wherein the pigment concentration of the first light shielding layer is lower than the pigment concentration of the second light shielding layer.

8. (Previously Presented) The plasma display panel of claim 7, wherein the pigment concentration of the first light shielding layer is lower by about  $1\% \sim 10\%$  than the pigment concentration of the second light shielding layer.

9. (Original) The plasma display panel of claim 1, wherein the pigment of the first and the second light shielding layers is a non-conductive pigment.

10. (Original) The plasma display panel of claim 9, wherein the pigment of the first and the second light shielding layers includes at least one of a cobalt oxide  $\text{Co}_x\text{O}_y$ , an iron oxide  $\text{Fe}_x\text{O}_y$ , a chrome oxide  $\text{Cr}_x\text{O}_y$  and a manganese oxide  $\text{Mn}_x\text{O}_y$ .

11. (Original) The plasma display panel of claim 9, wherein the concentration of the pigment is about 70% in the first and the second light shielding layers.

12. (Original) The plasma display panel of claim 1, wherein the pigment of the first light shielding layer comprises a conductive pigment.

13. (Currently Amended) The plasma display panel of claim 12, wherein the pigment of the first light shielding layer includes a ruthenium oxide  $\text{Ru}_x\text{O}_y$ , wherein x and y are positive integers.

14. (Original) The plasma display panel of claim 12, wherein the concentration of the pigment in the first light shielding layer is about 60% ~ 69%.

15. (Currently Amended) A plasma display panel, comprising:

a pair of first and second substrates spaced parallel to each other and sandwiching a discharge gas space filled with a discharge gas;

a plurality of ~~pairs of row electrodes extending horizontally and~~ first electrodes arranged on an internal surface of the first substrate;

a dielectric layer formed on ~~the~~ an internal surface of the first substrate and the plurality of ~~pairs of row~~ first electrodes; and

a plurality of ~~column electrodes extending vertically and~~ second electrodes arranged on an internal surface of the second substrate;

wherein the dielectric layer includes a plurality of black layers between two vertically adjacent discharge cells, and the plurality of black layers includes a first black layer formed between a portion of the ~~row~~ first electrodes and the first substrate, and a second black layer formed between ~~vertically adjacent row~~ first electrodes, and a thickness of the first black layer is less than 50% of ~~the~~ a thickness of the second black layer, wherein the second black layer has a same conductivity as the first black layer.

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16. (Currently Amended) A plasma display panel, comprising:

- a transparent electrode;
- a metal bus electrode;
- a first light shielding layer formed between the transparent electrode and the metal bus electrode on each discharge cell; and
- a second light shielding layer formed between the adjacent cells,

wherein each of the first and the second light shielding layers has a different light shielding ratio from each other, wherein a thickness of the first light shielding layer is thinner than a thickness of the second light shielding layer; and

a pigment concentration of the first light shielding layer is lower than a pigment concentration of the second light shielding layer.

17. (Previously Presented) The plasma display panel of claim 16, wherein the light shielding ratio of the first light shielding layer is lower than the light shielding ratio of the second light shielding layer.

18. (Previously Presented) The plasma display panel of claim 17, wherein the light shielding ratio of the first light shielding layer is lower by 0.1% ~ 5% than the light shielding ratio of the second light shielding layer.

19. (Currently Amended) The plasma display panel of claim 16, wherein the first light shielding layer and the second light shielding layer are different from each other in at least one of ~~[[a]]~~ the thickness and ~~[[a]]~~ the pigment concentration.

20. (Canceled)

21. (Previously Presented) The plasma display panel of claim 15, wherein the first black layer has a different pigment concentration than the second black layer.

22. (Previously Presented) The plasma display panel of claim 15, wherein the first black layer has a different light shielding ratio than the second black layer.

23. (Previously Presented) The plasma display panel of claim 15, wherein the first black layer contacts the second black layer.

24. (Previously Presented) The plasma display panel of claim 15, wherein the thickness of the first black layer is greater than 0.5% of the thickness of the second black layer.

25. (Previously Presented) The plasma display panel of claim 15, wherein the discharge gas comprises one of He, Xe or Ne.

26. (Currently Amended) The plasma display panel of claim 15, wherein at least one of the ~~row~~first electrodes comprises a metal bus electrode and a transparent electrode.

27. (Previously Presented) The plasma display panel of claim 26, wherein the first black layer is formed between the metal bus electrode and the transparent electrode.

28. (Previously Presented) The plasma display panel of claim 26, wherein a color of the first black layer is darker than the metal bus electrode.

29. (Previously Presented) The plasma display panel of claim 26, wherein the first black layer has a relatively low conductivity as compared to the metal bus electrode.

30. (Previously Presented) The plasma display panel of claim 15, wherein the thickness of the first black layer is between 0.1 and 2 micrometers.

31. (Previously Presented) The plasma display panel of claim 15, wherein the first black layer comprises a black-colored layer.

32. (Previously Presented) The plasma display panel of claim 15, wherein the first black layer is made of same materials as the second black layer.

33. (Previously Presented) The plasma display panel of claim 15, wherein the first black layer is connected to the second black layer.

34. (Previously Presented) The plasma display panel of claim 15, wherein the second black layer has the thickness between 2 and 4 micrometers.

35. (Canceled)

36. (Currently Amended) The plasma display panel of claim 15, wherein another dielectric layer is formed on the internal surface of the second substrate and the ~~column~~second electrodes.

37. (Previously Presented) The plasma display panel of claim 15, wherein the first black layer is formed on a surface of the area between the vertically adjacent discharge cells.

38. (Currently Amended) The plasma display panel of claim 15, wherein the second black layer is formed on a surface of the area between the ~~vertically adjacent discharge cells~~first electrodes.



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39. (Currently Amended) A color plasma display panel, comprising:

- a pair of first and second substrates spaced parallel to each other and sandwiching a discharge gas space filled with a discharge gas;
- a plurality of ~~pairs of row~~ first electrodes extending horizontally and arranged on an internal surface of the first substrate;
- a dielectric layer formed on the internal surface of the first substrate and the plurality of ~~pairs of row~~ first electrodes; and
- a plurality of ~~the column~~ second electrodes extending vertically and arranged on an internal surface of the second substrate,

wherein the dielectric layer includes a plurality of black layers between two vertically adjacent discharge cells,

the plurality of black layers include a first black layer formed ~~under~~ between a portion of one of the ~~row~~ first electrodes and the first substrate, and a second black layer formed outside of the ~~row~~ first electrodes, and

at least one portion of the first black layer has a same thickness as the second black layer.

40. (Currently Amended) The color plasma display panel of claim 39, wherein at least one of the ~~row~~ first electrodes comprises a metal bus electrode and a transparent electrode.

41. (Previously Presented) The color plasma display panel of claim 40, wherein the first black layer is formed between the metal bus electrode and the transparent electrode.

42. (Previously Presented) The color plasma display panel of claim 40, wherein a color of the first black layer is darker than the metal bus electrode.

43. (Previously Presented) The color plasma display panel of claim 40, wherein the first black layer has a relatively low conductivity as compared to the metal bus electrode.

44. (Previously Presented) The color plasma display panel of claim 39, wherein the thickness of the first black layer is between 0.1 and 2 micrometers.

45. (Previously Presented) The color plasma display panel of claim 39, wherein the second black layer has the thickness between 2 and 4 micrometers.

46. (Currently Amended) The color plasma display panel of claim 39, wherein the first black layer is made of the same materials as the second black layer.

47. (Previously Presented) The color plasma display panel of claim 39, wherein the first black layer is connected to the second black layer.